## AMENDMENT TO THE CLAIMS

Please amend the presently pending claims as follows:

- 1-17. (Canceled).
- 18. (Currently Amended) A power tester apparatus for testing an electronic device, the device configured to operate using a constant power supply voltage at a nominal power supply voltage, the apparatus comprising:
  - a <u>multi-voltage</u> power source <u>capable of</u> supplying <u>a</u>

    <u>selectable voltage level for thea</u> constant power

    supply voltage at <u>thea</u> nominal power supply

    voltage of <u>thean</u> electronic device;
  - a connector coupled to the power source, the connector adapted to connect the constant power supply voltage to a power supply input on the electronic device;
  - circuitry configured to introduce controllable disturbances into the constant power supply voltage—applied to the electronic device, a disturbance configured to simulate a disruption in the nominal power supply voltage; and
  - wherein the disturbances introduced into the constant

    power supply voltage applied to the electronic

    device are controllable.
  - an additional power source capable of supplying an additional voltage,

## wherein the additional voltage is outside a range of the selectable voltage level that the multi-voltage power source can supply.

- 19. (Previously Presented) The apparatus of claim 18 wherein the disturbance is a rising pulse having a maximum voltage which is controllable.
- wherein the disturbance is a low-going pulse having a minimum voltage being less than the nominal power supply voltage.
  - 21. (Previously Presented) The apparatus of claim 18 wherein the constant power supply voltage is selected from the group of voltages consisting of +5 VDC and +12 VDC.
  - 22. (Canceled).
  - 23. (Currently Amended) The apparatus of claim  $\frac{2218}{1}$  wherein the additional voltage is + 24 VDC.
  - 24. (Currently Amended) The apparatus of claim 18 including a manually operated user interface used to control the <u>disturbancesdisturbance</u>.
  - 25. (Previously Presented) The apparatus of claim 18 wherein the disturbance is at least one pulse having a duration and a magnitude which are controllable.

- 26. (Previously Presented) The apparatus of claim 18 wherein the disturbance is a plurality of pulses and a frequency and a number of pulses in the plurality of pulses are controllable.
- 27. (Previously Presented) The apparatus of claim 18 wherein the disturbance comprises a voltage sequence applied during powering up of the electronic device.

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- 28. (Currently Amended) A method for testing an electronic device of the type which is powered by a constant power supply voltage at a nominal power supply voltage, the method comprising:
  - supplying a selectable voltage level for thea constant power supply voltage at thea nominal power supply voltage of thean electronic device from a multivoltage power source;
    - coupling the constant power supply voltage to a
       connector, the connector adapted to connect the
       constant power supply voltage to a power supply
       input of the electronic device;
    - introducing a disturbance into the constant power supply voltage—applied to the power supply input of the electronic device; and
    - controlling the disturbance—<u>introduced into the</u>

      <del>constant power supply voltage applied to the</del>

      <del>power supply to simulate a disruption in the</del>

      <del>nominal power supply voltage.</del>; and
    - supplying an additional voltage from an additional
      power source,

- wherein the additional voltage is outside a range of
  the selectable voltage level that the multivoltage power source can supply.
- 29. (Previously Presented) The method of claim 28 wherein the disturbance is a rising pulse having a maximum voltage which is controllable.
- 30. (Previously Presented) The method of Sclaim 28 wherein the disturbance is a low-going pulse voltage which is controllable.

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31. (Currently Amended) The method of claim 28 wherein the nominal constant power supply voltage is selected from the group of voltages consisting of +5 VDC and ±12 VDC.

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32. (Canceled).

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- 33. (previously presented) The method of claim  $\frac{3228}{}$  wherein the additional voltage is + 24 VDC.
- 34. (Previously Presented) The method of claim 28 including receiving control parameters from the user interface.
- 35. (Previously Presented) The method of claim 28 wherein the disturbance is a pulse having a controllable duration and a controllable magnitude.

36. (Previously Presented) The method of claim 28 wherein the disturbance is a plurality of pulses and a number of the plurality of pulses are controllable.

37. (Canceled).

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